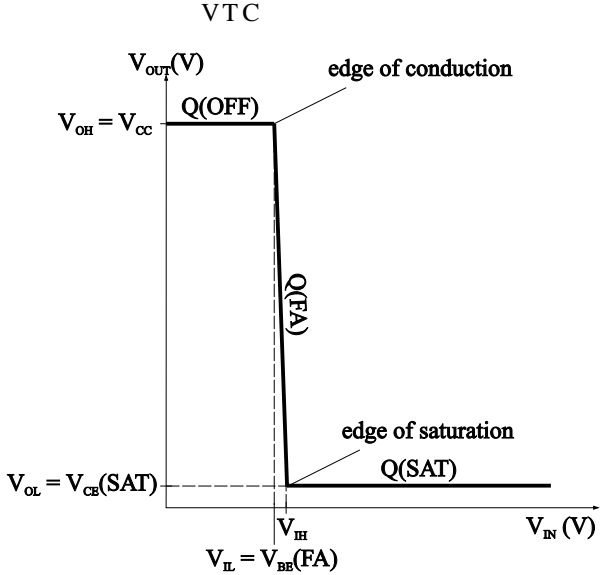
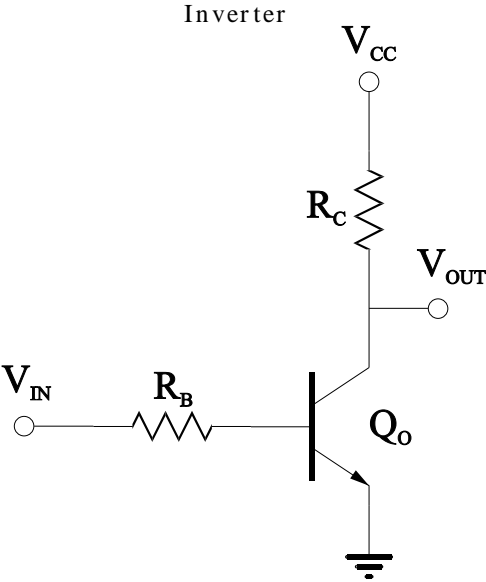


# Resistor Transistor Logic(RTL)



## RTL Gates

