

GEORGIOS FAINEKOS

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RESEARCH INTERESTS

Cyber-Physical Systems: *Theoretical Foundations, Hybrid Dynamical Systems, Real Time and Embedded Systems*; **Formal Methods with applications to Control & Automation:** *Controller Synthesis, System Testing and Verification, Formal Languages and Logic*; **Robotics:** *Motion and Mission Planning, Human-Robot Interfaces*.

EDUCATION

- **University of Pennsylvania** (2004 – 2008), Philadelphia, PA, USA
Doctor of Philosophy in Computer and Information Science
Thesis: Robustness of Temporal Logic Specifications
Advisor: G. J. Pappas (UPenn) GRASP Lab
Committee: Rajeev Alur (Chair), Edmund M. Clarke (External), Insup Lee, and Oleg Sokolsky
- **University of Pennsylvania** (2002 – 2004), Philadelphia, PA, USA
Master of Science in Engineering in Computer and Information Science
- **National Technical University of Athens** (1996 – 2001), Athens, Greece
Diploma (Master of Science in Engineering) in **Mechanical Engineering**; Major in **Design & Control**;
Rank in graduating class: 11/137
Thesis: Ant Colony Optimization: Applications to discrete and continuous problems
Advisor: K. Giannakoglou (NTUA) Lab of Thermal Turbomachines
Committee: K. Giannakoglou (Chair), K. Mathioudakis, K. Papailiou
- **University of Patras** (1995 – 96), Patra, Greece
Mathematics
- **2nd High School of Agios Dimitrios** (Graduation 1995), Athens, Greece

ACADEMIC EMPLOYMENT

- **Associate Professor**, Aug. 2015 – up to now
Arizona State University, Tempe, USA
Appointment: School of Computing, Informatics and Decision Systems Engineering.
Director: Cyber-Physical Systems Laboratory (CPSLab)
Affiliation: NSF I/UCRC Center for Embedded Systems (CES)
- **Assistant Professor**, Aug. 2009 – 2015
Arizona State University, Tempe, USA
Appointment: School of Computing, Informatics and Decision Systems Engineering.
Director: Cyber-Physical Systems Laboratory (CPSLab)
Affiliation: NSF I/UCRC Center for Embedded Systems (CES)

PROFESSIONAL EMPLOYMENT

- **Postdoctoral Researcher**, NEC Laboratories America, Oct. 2008 – Aug. 2009, USA
Principal researcher for the project Tessa (Techniques for Embedded System and Software Assurance): see Research Experience section.
- **Mechanical Engineer**, Jet Engineering ltd, Nov. 2001 – Jul. 2002, Greece
Part of an engineering team working on the design, verification, validation and manufacturing of tank trailers and tank containers for hazardous materials. Successfully designed (using SolidWorks) three out of the four tank models of the company's current production line. Design for adherence to international standards: ADR, PrEN-13094, PrEN-14025, DIN-4100 and EN-288. Trained in verification using finite element methods and in experimental validation (data acquisition from the prototype tank vehicles).
- **Intern**, Microfluidics Laboratory, Purdue University, Aug. – Dec. 2000, USA
Part of a group of students working on the design of a manifold supporting a model (microchip) of the choroidal vasculature. Responsibilities: Design of the model in Pro Engineer; Web page development.
- **Intern**, Engines Maintenance Unit, Hellenic Aerospace Industry ltd, Mar. – Jul. 2000, Greece
Development of an automated Local Heat Treatment (LHT) unit for military engine parts. Trained on LHT processes for aerospace materials. Successfully designed and implemented in Visual Basic a software package for the control of LHT processes. Tuning of the PID controllers. Selected components and designed procedures in accordance with military specifications. Final report (in Greek) co-supervised by Dr. Freskos (HAI) and Dr. Giannakoglou (NTUA): *Local Heat Treatment Processes: Development of computer software for Control and Data Acquisition*.
- **Intern**, Current Technology ltd, 1996 – 99, Greece
Participated in various posts in summer internships in CT ltd which is specialized in the design and production of industrial automations and CNC machines. Sample projects: plasma cutting and milling CNC machines, foam cutting machines, jet-engine testing facilities.

RESEARCH ACTIVITIES AND EXPERIENCE

- **Assistant Professor**, Arizona State University, Aug. 2009 –, USA
Further information can be found at:
<http://www.public.asu.edu/~gfaineko/research.html>.

Active research programs

- (AP1) **CPS:Synergy: Collaborative Research: Collaborative Vehicular Systems, PI: G. Fainekos**, ASU; Funding source: National Science Foundation (NSF), Award # 1446730, Period: Jan. 2015 - Dec. 2017, **Award: \$294,136**. (Collaborative with Umit A Ozguner, Ohio State University, Total award for period Jan. 2015 - Dec. 2017: \$979,036)
Brief Description: As self-driving cars are being introduced into road networks, the overall safety and efficiency of the resulting traffic system must be guaranteed. This project develops methods to analyze and coordinate networks of fully and partially self-driving vehicles that interact with conventional human-driven vehicles on intelligent road grids. The resulting comprehensive framework will be able to model, analyze and test reconfiguration, hierarchical control, security and trust in these smart transportation systems.
- (AP2) **Automated Testing for Functional Coverage for Cyber-Physical Systems, Single PI: G. Fainekos**, ASU; Funding source: Center for Embedded Systems (CES) at Arizona State University (ASU), Main supporting CES industry member: Toyota, Bosch, Award # A1.Y6.GF, Period: Aug. 2014 – Jul. 2016, Total amount: \$141,000, **recognized amount: \$118,000** (Administratively the co-PI is the CES director Dr. Vrudhula).
Brief Description: The project develops automated testing methods for Cyber-Physical Systems (CPS). The focus is on systems which are modeled within the Simulink/Stateflow environment and which are characterized by both continuous and discrete dynamics. The goal is to develop methods that guarantee coverage of the state space induced by the discrete system dynamics.

- (AP3) **CAREER: Robustness Guided Testing and Verification for Cyber-Physical Systems, Single PI: G. Fainekos**, ASU; Funding source: National Science Foundation (NSF), Award # 1350420, Period: Aug. 2014 - Jul. 2019, **Total Award: \$ 436,857.00**.
Brief Description: Developing a comprehensive theory for testing and verification in all the stages of a Model Based Development process for a Cyber-Physical System. The ultimate goal is to support component reuse, incremental system improvements and modular design. The project's research comprises three components: development of conditions on the algorithms and on the structure of the CPS for inferring finite-time guarantees on the randomized testing process; the study of testing methods that can support modular and compositional system design; and investigation of appropriate notions of conformance between two system models and between a model and its implementation on a computational platform.
- (AP4) **CSR: Small: Collaborative Research: Gray Box Testing of Complex Cyber-Physical Systems Using Optimization and Optimal Control Techniques, Single PI at ASU: G. Fainekos**, ASU; Funding source: National Science Foundation (NSF), Award # 1319560, Period: Oct. 2013 - Sep. 2016, **Award: \$249,998.00**. (Collaborative with Sriram Sankaranarayanan, University of Colorado, Total award for period Oct. 2013 - Sep. 2016: \$499,374.00)
Brief Description: Developing new methodologies for gray-box testing models of Cyber-Physical Systems. Ideas from optimization and optimal control theory are employed in order to drive the process of state-space exploration for system verification. The driving factor of the project is to utilize the minimum possible information from the models.

Completed research programs

- (CP1) **CSR: Small: Model Exploration for Cyber-Physical Systems & REU supplement, Single PI: G. Fainekos**, ASU; Funding source: National Science Foundation (NSF), Award # 1116136, Period: Aug. 2011 - Jul. 2015, **Award: \$390,998.00**.
Brief Description: Developing a general theory for property exploration of systems. Property exploration attempts to discover the properties that are satisfied by the system. The theory can address finite state models, continuous models and hybrid models (both discrete and continuous) using either model-based analysis or black-box techniques. The properties being discovered are restricted in the class of linear-time (metric or not) temporal logics. The project outcomes are being implemented in various software tools depending on the application domain.
- (CP2) **Parallelization of Embedded Control Applications on Multi-core Architectures: A Case Study, PI: G. Fainekos**, ASU; **co-PI: Yann-Hang Lee**, ASU; Funding source: Center for Embedded Systems (CES) at Arizona State University (ASU), Main supporting CES industry member: Toyota, Award # A2.Y5.GF.YHL & TBD.Y6.GF.YHL, Period: Aug. 2013 – Jul. 2015, Total amount: \$94,231, **recognized amount: \$32,039** (Administratively another co-PI is the CES director Dr. Vrudhula).
Brief Description: Studying the parallelization of embedded control applications on multi-core architectures. Developing algorithms for mapping sequential programs to multi-core platforms within a Model-Based Development framework. Building experimental testbeds for evaluation.
- (CP3) **I-Corps: Formal Specification Driven Verification and Validation Framework for Cyber-Physical Systems, Single PI: G. Fainekos**, ASU; Funding source: National Science Foundation (NSF), Award # 1454143, Period: Sep. 2014 - Feb. 2015, **Total Award: \$ 50,000.00**.
Brief Description: This project aims to commercialize the academic tool S-TaLiRo, a software tool for the verification and testing of Cyber-Physical Systems (CPS). S-TaLiRo provides such capabilities by enabling automatic test generation and verification guided by formal specifications expressed in temporal logics. It can analyze large and complex Simulink models, user-defined functions and blackbox models. S-Taliro provides functionality for specification falsification, parameter estimation, conformance testing and runtime specification monitoring.
- (CP4) **Collaborative Research: Consortium for Embedded Systems: REU supplement, PI: Sarma K Vrudhula**, ASU; **co-PI G. Fainekos**, ASU; Funding source: National Science Foundation (NSF), Award # 0856090, Period: Sept. 2012 - Aug. 2013, Total amount: \$15,923, **recognized amount: \$15,764**.

Brief Description: The project supports undergraduate students for research in the PI Fainekos' projects funded by the Center for Embedded Systems (CES) at Arizona State University (ASU).

- (CP5) **Visual Interface for Metric Temporal Logic Specifications, PI: G. Fainekos, ASU; co-PI:** Yoshihiro Kobayashi, ASU; Funding source: Center for Embedded Systems (CES) at Arizona State University (ASU), Main supporting CES industry member: Toyota, Award # A1.Y5.GF.YK, Period: Aug. 2013 – Jul. 2014, Total amount: \$48,231, **recognized amount: \$16,399** (Administratively another co-PI is the CES director Dr. Vrudhula).

Brief Description: Developing graphical user interfaces for stating Metric Temporal Logic specifications. The graphical user interface is needed in order to enable the use of S-TaLiRo tools by non-expert logic users.

- (CP6) **Temporal Logic Testing for Stochastic Cyber-Physical Systems, Single PI: G. Fainekos, ASU;** Funding source: Center for Embedded Systems (CES) at Arizona State University (ASU), Main supporting CES industry member: Toyota, Award # A1.Y4.GF, Period: Aug. 2012 – Jul. 2013, Total amount: \$47,000, **recognized amount: \$23,500** (Administratively the co-PI is the CES director Dr. Vrudhula).

Brief Description: Developed a framework for robustness-guided model checking for Stochastic Cyber-Physical Systems with respect to Metric Temporal Logic specifications. The results were incorporated in the Matlab/Simulink toolbox S-TaLiRo.

- (CP7) **Parallelization of Embedded Control Applications on Multi-core Architectures: A Case Study, PI: G. Fainekos, ASU; co-PI: Karam S. Chatha, ASU;** Funding source: Center for Embedded Systems (CES) at Arizona State University (ASU), Main supporting CES industry member: Toyota, Award # A2.Y4.KC.GF, Period: Aug. 2012 – Jul. 2013, Total amount: \$47,000, **recognized amount: \$15,980** (Administratively another co-PI is the CES director Dr. Vrudhula).

Brief Description: Developed a theory for quantifying the approximate functional equivalence between two Cyber-Physical Systems. Stochastic optimization methods are utilized in order to compute the approximate distance between two systems. The framework has been incorporated into S-TaLiRo and it can be utilized for studying the functional equivalence between a sequential and a parallelized version of an embedded control system.

- (CP8) **Statistical Techniques for Property Exploration of Cyber-Physical Systems, Single PI: G. Fainekos, ASU;** Funding source: Center for Embedded Systems (CES) at Arizona State University (ASU), Main supporting CES industry member: Toyota, Award # A2.Y3.GF, Period: Aug. 2011 – Jul. 2012, **Award: \$45,915.00**.

Brief Description: Developed black-box property exploration methods for Cyber-Physical Systems. Property exploration attempts to derive the properties that are satisfied by the system. The framework utilized parametric temporal logics and randomized falsification methodologies. The focus was on systems modeled within the Matlab/Simulink environment and the project outcomes were incorporated in the S-TaLiRo toolbox.

- (CP9) **SHF: Small: Collaborative Research: Statistical Techniques for Verifying Temporal Properties of Embedded and Mixed-Signal Systems, Single PI at ASU: G. Fainekos, ASU;** Funding source: National Science Foundation (NSF), Award # 1017074, Period: Aug. 2010 - Jul. 2013, **Award: \$239,319.00**. (Collaborative with Sriram Sankaranarayanan, University of Colorado, Total award for period Aug. 2010 - Jul. 2013: \$488,921.00)

Brief Description: Developed the theory of robustness-guided model checking for black-box models of Cyber-Physical Systems. Formal specifications in Metric Temporal Logic are given quantitative interpretation which acts as a cost function for an optimization problem. Minimizing the quantitative interpretation is equivalent to detecting the least robust system behavior with respect to the specification. Developed algorithms with formal guarantees on the testing and verification performance. Developed the Matlab/Simulink toolbox S-TaLiRo.

- (CP10) **Robust Testing for Reconfigurable Networked Control Systems and Mixed-Signal Systems, Single PI: G. Fainekos, ASU;** Funding source: Center for Embedded Systems (CES) at Arizona State University (ASU), Main supporting CES industry member: Toyota, Awards # A2.Y1.GF and A2.Y2.GF.C, Period: Jan. 2010 – Dec. 2011, **Award: \$69,497.00**.

Brief Description: Developed model-based testing and verification techniques for Networked Control Systems (NCS) and Mixed-Signal Systems. The developed framework utilized the theory of robust testing, i.e., a single trajectory represents a neighborhood of system trajectories, and randomized falsification methodologies based on stochastic optimization methods.

- **Postdoctoral Researcher**, NEC Laboratories America, Oct. 2008 – Aug. 2009, USA

- *Tessa: Techniques for Embedded System and Software Assurance*

Research on verification and testing of embedded systems software. Developed a framework for the robustness analysis of Simulink simulations and models using self-validated arithmetics (interval and affine arithmetic). The goal of the project was to study the robustness and correctness of Simulink simulations under system uncertainties, numerical errors and floating point rounding errors. The outcome of the project was the Matlab toolbox RobSim which can analyze continuous-time, discrete-time and mixed-signal systems.

- **Research Assistant**, GRASP Lab, University of Pennsylvania, 2002 – 2008, USA

- *Formal Methods for Testing and Verification of Hybrid and Dynamical Systems*

Developed a theory for the robustness of temporal logic specifications over discrete and continuous time signals in metric spaces. Built a software toolbox for analyzing the robustness of temporal logic specifications over discrete time signals. Developed a framework for reasoning in continuous time using discrete time methods. Applied the theory to the analysis of continuous – discrete time systems with metric state spaces. Built a software toolbox for testing hybrid systems.

- *Controller Design Using Temporal Logic Specifications*

Developed a framework for deriving controller specifications from temporal logic formulas. Built a toolbox that synthesizes hybrid systems from temporal logic specifications.

- *From Structured English to Path Planning for Mobile Robots*

Developed a framework for deriving hybrid controllers for path planning for mobile robots from structured English. The framework uses as intermediate formalism linear temporal logic and applies to motion planning of both dynamic and kinematic models of robots.

- *Modeling and Development of Unmanned Aerial Vehicles (UAV)*

Developed aerodynamic and structural models of the Penn UAV platform. Designed and built parts of the Penn UAV platform.

- **Research Assistant**, Lab of Thermal Turbomachines, NTUA, Jan. – Nov. 2001, Greece

- *Inverse design of airfoils using Ant Colony Optimization algorithms*

Extended the discrete ant colony optimization algorithm to handle continuous optimization problems. Built a software toolbox for the application of the method to the problem of inverse design of airfoils.

AWARDS AND HONORS

- Honorable Mention at Cornell Cup 2014 to the NAO Navigators team (ASU SCIDSE Undergraduates: Mila Arezina, Joe Boeding, Bijan Fakhri, Sami Mian, Ryan James Sterry, and Cameron Stewart).

<https://www.facebook.com/pages/Cornell-Cup-USA-presented-by-Intel/115084925265002>

- 2014 technological breakthrough nomination by the industry for CES-funded research outcomes.

http://faculty.washington.edu/scottcs/NSF/2014/NSF_Compendium_2014.pdf

- Finalist for the Best Student Paper Award (ASU students Houssam Abbas and Bardh Hoxha) at the 2014 IEEE International Conference on CYBER Technology in Automation, Control, and Intelligent Systems (CYBER) for the paper titled “*Robustness-Guided Temporal Logic Testing and Verification for Stochastic Cyber-Physical Systems.*”

- 2013 NSF CAREER Award for the project titled: “*Robustness Guided Testing and Verification for Cyber-Physical Systems.*”
Citation from NSF website: “*The Faculty Early Career Development (CAREER) Program is a Foundation-wide activity that offers the National Science Foundation’s most prestigious awards in support of junior faculty who exemplify the role of teacher-scholars through outstanding research, excellent education and the integration of education and research within the context of the mission of their organizations.*”
http://www.nsf.gov/awardsearch/showAward?AWD_ID=1350420
- 2013 Best Researcher Junior Faculty award for the School of Computing Informatics and Decision Systems Engineering at Arizona State University
- 2012 technological breakthrough nomination by the industry for CES-funded research outcomes.
http://faculty.washington.edu/scottcs/NSF/2012/NSF_Compendium_2012-WEB.pdf
- 2008 Frank Anger Memorial ACM SIGBED/SIGSOFT Student Award
Citation from the SIGBED website: “*The Frank Anger Memorial Award is a student award in the name of late Dr. Frank Anger to promote cross-disciplinary research between embedded systems and software engineering.*”
<http://sigbed.blogspot.com/p/awards.html>
- Finalist for the Best Student Paper Award (student authors Hadas Kress-Gazit and Georgios Fainekos) at the 2007 IEEE International Conference on Robotics and Automation (ICRA) for the paper titled “*Where’s Waldo? Sensor-Based Temporal Logic Motion Planning.*”
- Best Poster Award at the 2005 Graduate Research Symposium at the University of Pennsylvania for the poster titled “*Temporal Logic Motion Planning for Mobile Robots.*”
- 2002 – 08: Graduate Research Fellowship (University of Pennsylvania).
- Award for academic excellence in engineering sciences for the academic year 2000-2001.
Awarded by the Technical Chamber of Greece and it is given to the top 5 students of every year in each department (for my class that corresponds to top 4 percentile).
- Several awards for “Best academic performance in class” and “Overall high GPA” during middle school and high school in Greece.

IN THE NEWS

- ASU Full Circle, Jun. 24, 2014: “*ASU teams robot driver in top 10 at Cornell Cup competition*” by Mayank Prasad
<http://fullcircle.asu.edu/2014/06/10746/>
- ASU Full Circle, Mar. 3, 2014: “*Fainekos work on embedded cyber-physical systems earns NSF CAREER award*” by Joe Kullman
<http://fullcircle.asu.edu/2014/03/10207/>
- Embedded Computing Design, Feb. 12th, 2014: “*Improving automotive safety system effectiveness with model-based development*” by Monique Devoe
<http://embedded-computing.com/articles/improving-system-effectiveness-model-based-development/>

PUBLICATIONS

Most of my publications are available on line at <http://www.public.asu.edu/~gfaineko/papers.html> through links to publishers and/or to preprint PDF files.

My own name appears in boldface. After my graduation in 2008: Graduate students whose thesis I supervise/ed or whose research I funded, appear in boldface and underlined. Undergraduate students whose thesis I supervise/ed or whose research I funded, appear in boldface and double underlined. The names of industrial collaborators are indicated with a †. Other students that I have collaborated with are indicated by a ‡.

For citation information see <http://scholar.google.com/citations?user=WGRYgBEAAAAJ&hl=en>.

Thesis

- (Th1) **Georgios Fainekos**, Robustness of Temporal Logic Specifications, PhD Thesis, Department of Computer and Information Science, University of Pennsylvania, August 2008; Advisor: George J. Pappas; Committee: Rajeev Alur (Chair), Edmund M. Clarke (External), Insup Lee, and Oleg Sokolsky.
- (Th2) **Georgios Fainekos**, Ant Colony Optimization: Applications to discrete and continuous problems, Diploma Thesis, Department of Mechanical Engineering, National Technical University of Athens, September 2001 (in Greek); Committee: K. Giannakoglou (Chair/Advisor), K. Mathioudakis, K. Papailiou.

Editorials

- (E1) **G. E. Fainekos**, E. Goubault, F. Ivancic and S. Sankaranarayanan, Guest editors for the special issue on Numerical Software Verification of Cyber-Physical Software Systems in *ACM Transactions on Embedded Computing Systems*, V 11, I S2, Aug 2012
- (E2) **G. E. Fainekos**, E. Goubault, S. Putot and S. Ratschan, Guest editors for the special issue on Numerical Software Verification in *Mathematics in Computer Science*, Vol. 5, N. 4, Springer 2011

Peer-Reviewed Journal Publications

- (J1) **Kangjin Kim**, **Georgios Fainekos** and Sriram Sankaranarayanan, On the Minimal Revision Problem of Specification Automata, *International Journal of Robotics Research*, vol. 34, no. 12, pp 1515-1535, October 2015
- (J2) **H. Abbas**, **G. Fainekos**, S. Sankaranarayanan, F. Ivancic[†], and A. Gupta[†], Probabilistic Temporal Logic Falsification of Cyber-Physical Systems, *ACM Transactions on Embedded Computing Systems*, V 12, I S2, May 2013
- (J3) **G. Fainekos** and G. J. Pappas, Robustness of temporal logic specifications for continuous-time signals. *Theoretical Computer Science*, Elsevier, V 410, N 42, pp 4262-4291, 2009.
- (J4) **G. Fainekos**, A. Girard, H. Kress-Gazit and G. J. Pappas, Temporal Logic Motion Planning for Dynamic Mobile Robots. *Automatica*, Elsevier, V 45, N 2, pp 343-352, 2009.
- (J5) H. Kress-Gazit, **G. Fainekos** and G. J. Pappas, Temporal Logic-based Reactive Mission and Motion Planning. *IEEE Transactions on Robotics*, IEEE, V 25, N 6, pp 1370-1381, 2009.
- (J6) H. Kress-Gazit, **G. Fainekos** and G. J. Pappas, Translating Structured English to Robot Controllers. *Advanced Robotics*, VSP\Brill Academic Publishers, V 22, N 12, pp 1343-1359, 2008. (**Invited paper**)
- (J7) **G. Fainekos** and K. C. Giannakoglou, Inverse Design of Airfoils Based on a Novel Formulation of the Ant Colony Optimization Method. *Inverse Problems in Engineering*, Taylor & Francis, V 11, N 1, pp 21-38, 2003.

Peer-Reviewed Conference and Workshop Papers with Indexed Proceedings

- (C1) Adel Dokhanchi, Bardh Hoxha, and **Georgios Fainekos**, Metric Interval Temporal Logic Specification Elicitation and Debugging. In the proceeding of *13th ACM-IEEE International Conference on Formal Methods and Models for System Design*, Austin, Texas, September 2015
- (C2) Bardh Hoxha, Nikolaos Mavridis, and **Georgios Fainekos**, VISPEC: A graphical tool for elicitation of MTL requirements. Proceedings of the *IEEE/RSJ International Conference on Intelligent Robots and Systems*, Hamburg, Germany, September 2015
- (C3) K. Kim, J. Campbell, W. Duong, Y. Zhang, and **G. Fainekos**, DisCoF⁺: Asynchronous DisCoF with Flexible Decoupling for Cooperative Pathfinding in Distributed Systems. *IEEE International Conference on Automation Science and Engineering*, Gothenburg, Sweden, August 2015
- (C4) C. E. Tuncali, **G. Fainekos**, Y.-H. Lee, Automatic Parallelization of Simulink Models for Multi-core Architectures. 12th IEEE International Conference on Embedded Software and Systems, New York, NY, August 2015
- (C5) Houssam Abbas, Hans Mittelmann and **Georgios Fainekos**, Formal property verification in a conformance testing framework. In the Proceedings of the *12th ACM-IEEE International Conference on Formal Methods and Models for System Design*, Lausanne, Switzerland, October 2014 (**28% Acceptance Rate for regular papers**)
- (C6) Adel Dokhanchi, Bardh Hoxha and **Georgios Fainekos**, On-Line Monitoring for Temporal Logic Robustness, In the Proceedings of the *Runtime Verification*, Toronto, Canada, September 2014 (**30% Acceptance Rate**)
- (C7) Yu Zhang, Kangjin Kim and **Georgios Fainekos**, DisCoF: Cooperative Pathfinding in Distributed Systems with Limited Sensing and Communication Range, In the Proc. of the *International Symposium on Distributed Autonomous Robotic Systems*, Daejeon, Korea, Nov 2014
- (C8) Kangjin Kim and **Georgios Fainekos**, Revision of Specification Automata under Quantitative Preferences, In the Proc. of the *IEEE International Conference on Robotics and Automation*, Hong Kong, China, June 2014
- (C9) Houssam Abbas, Andrew Winn[‡], **Georgios Fainekos**, and A. Agung Julius, Functional Gradient Descent Method for Metric Temporal Logic Specifications, In the Proceedings of the *American Control Conference*, Portland, OR, June 2014, (**Invited paper**)
- (C10) Houssam Abbas, Bardh Hoxha, **Georgios Fainekos** and Koichi Ueda[†], Robustness-Guided Temporal Logic Testing and Verification for Stochastic Cyber-Physical Systems, In the Proceedings of the *IEEE International Conference on CYBER Technology in Automation, Control, and Intelligent Systems*, Hong Kong, China, June 2014 (**Finalist for Best Student Paper**)
- (C11) Houssam Abbas and **Georgios Fainekos**, Computing Descent Direction of MTL Robustness for Non-Linear Systems, In the Proceedings of the *American Control Conference*, Washington DC, June 2013, (**Invited paper**)
- (C12) S. Srinivas, R. Kermani, K. Kim, Y. Kobayashi and **G. Fainekos**, A Graphical Language for LTL Motion and Mission Planning, In the Proc. of the *IEEE International Conference on Robotics and Biomimetics*, Shenzhen, China, Dec. 2013
- (C13) Kangjin Kim and **Georgios Fainekos**, Minimal Specification Revision for Weighted Transition Systems, In the Proc. of the *IEEE International Conference on Robotics and Automation*, Karlsruhe, Germany, May 2013
- (C14) Hengyi Yang, Bardh Hoxha and **Georgios Fainekos**, Querying Parametric Temporal Logic Properties on Embedded Systems, *Int. Conference on Testing Software and Systems*, Lecture Notes in Computer Science, Vol. 7641, pp 136-151, Springer 2012 (**33% Acceptance Rate**)
- (C15) Kangjin Kim and **Georgios Fainekos**, Approximate Solutions for the Minimal Revision Problem of Specification Automata, In the Proc. of the *IEEE/RSJ International Conference on Intelligent Robots and Systems*, Vilamoura Algarve, Portugal, Oct. 2012

- (C16) Sriram Sankaranarayanan and **Georgios Fainekos**, Simulating Insulin Infusion Pump Risks by In-Silico Modeling of the Insulin-Glucose Regulatory System, In the Proc. of the *10th Conference on Computational Methods in Systems Biology*, Lecture Notes in Computer Science, Vol. 7605, pp 322-341, Springer 2012
- (C17) **Kangjin Kim**, **Georgios Fainekos** and Sriram Sankaranarayanan, On the Revision Problem of Specification Automata, In the Proceedings of the *IEEE Conference on Robotics and Automation*, St. Paul, Minnesota, May 2012, (**Invited paper**)
- (C18) **Georgios Fainekos**, Sriram Sankaranarayanan, Koichi Ueda[†] and Hakan Yazarel[†], Verification of Automotive Control Applications using S-TaLiRo, In the Proceedings of the *American Control Conference*, Montreal, Canada, June 2012, (**Invited paper**)
- (C19) Sriram Sankaranarayanan and **Georgios Fainekos**, Falsification of Temporal Properties of Hybrid Systems Using the Cross-Entropy Method, In the Proceedings of the *ACM International Conference on Hybrid Systems: Computation and Control*, Beijing, China, Apr. 2012
- (C20) A. Chakarov[‡], S. Sankaranarayanan, and **G. Fainekos**, Combining Time and Frequency Domain Specifications For Periodic Signals, In the Proc. of *Runtime Verification*, Lecture Notes in Computer Science, Vol. 7186, pp 294-309, Springer 2012
- (C21) **H. Abbas** and **G. Fainekos**, Linear Hybrid System Falsification Through Local Search, In the Proc. of *Automated Technology for Verification and Analysis*, Lecture Notes in Computer Science, Vol. 6996, pp 503-510, Springer 2011
- (C22) **Y. S. R. Annapureddy**, **C. Liu**, **G. E. Fainekos** and S. Sankaranarayanan, S-TaLiRo: A Tool for Temporal Logic Falsification for Hybrid Systems, In the Proc. of *Tools and algorithms for the construction and analysis of systems*, Lecture Notes in Computer Science, Vol. 6605, pp 254-257, Springer 2011 (**28% Acceptance Rate**)
- (C23) Truong Nghiem[‡] and **Georgios E. Fainekos**, Computing Schedules for Time-Triggered Control using Genetic Algorithms, In the *Proceedings of the 18th IFAC World Congress*, Milan, Italy, Aug. 2011
- (C24) **G. E. Fainekos**, Revising Temporal Logic Specifications for Motion Planning. In the *Proceedings of the 2011 IEEE International Conference on Robotics and Automation*, Shanghai, China, May 2011
- (C25) **Y. S. R. Annapureddy** and **G. E. Fainekos**, Ant Colonies for Temporal Logic Falsification of Hybrid Systems, In the *Proceedings of the 36th Annual Conference of IEEE Industrial Electronics*, pp. 91-96, Glendale, AZ, Nov. 2010
- (C26) Truong Nghiem[‡], S. Sankaranarayanan, **G. Fainekos**, F. Ivancic, A. Gupta and G. Pappas, Monte-Carlo Techniques for Falsification of Temporal Properties of Non-Linear Systems, In the *Proceedings of the 13th ACM Conference on Hybrid Systems: Computation and Control*, Stockholm, Sweden, Apr. 2010 (**28% Acceptance Rate**)
- (C27) **G. E. Fainekos**, S. Sankaranarayanan, F. Ivancic and A. Gupta, Robustness of Model-based Simulations. *IEEE Real-Time Systems Symposium*, pp 345 - 354, Washington DC, Dec. 2009 (**21% Acceptance Rate**)
- (C28) **G. E. Fainekos** and G. J. Pappas, MTL Robust Testing and Verification for LPV Systems. In the *Proceedings of the 2009 IEEE/AACC American Control Conference*, pp 3748 - 3753, St. Louis, Missouri, June 2009
- (C29) **G. E. Fainekos** and G. J. Pappas, Robust Sampling for MITL Specifications. *In the 5th Inter. Conference on Formal Modeling and Analysis of Timed Systems*, Lecture Notes in Computer Science, Vol. 4763, pp 147-162, Springer 2007
- (C30) **G. E. Fainekos**, A. Girard and G. J. Pappas, Hierarchical Synthesis of Hybrid Controllers from Temporal Logic Specifications. *Hybrid Systems: Computation and Control*, Lecture Notes in Computer Science, Vol. 4416, pp 203-216, Springer 2007 (**25% Acceptance Rate**)

- (C31) A. A. Julius, **G. E. Fainekos**, M. Anand, I. Lee and G. J. Pappas, Robust test generation and coverage for hybrid systems. *Hybrid Systems: Computation and Control*, Lecture Notes in Computer Science, Vol. 4416, pp 329-342, Springer 2007 (**25% Acceptance Rate**)
- (C32) H. Kress-Gazit, **G. E. Fainekos** and G. J. Pappas, From Structured English to Robot Motion. In the *Proceedings of the 2007 IEEE/RSJ International Conference on Intelligent Robots and Systems*, pp. 2717-2722, San Diego, California, October 2007
- (C33) H. Kress-Gazit, **G. E. Fainekos** and G. J. Pappas, Where's Waldo? Sensor-Based Temporal Logic Motion Planning. In the *Proceedings of the 2007 IEEE International Conference on Robotics and Automation*, pp. 3116-3121, Rome, Italy, April 2007 (**Finalist for best student paper, Invited paper**)
- (C34) **G. E. Fainekos**, A. Girard and G. J. Pappas, Temporal Logic Verification Using Simulation. In the *4th Inter. Conference on Formal Modeling and Analysis of Timed Systems*, Lecture Notes in Computer Science, Vol. 4202, pp 171-186, Springer 2006
- (C35) **G. E. Fainekos** and G. J. Pappas, Robustness of Temporal Logic Specifications. In the *Workshop on Formal Approaches to Testing and Runtime Verification*, Lecture Notes in Computer Science, Vol. 4262, pp 178-192, Springer 2006
- (C36) **G. E. Fainekos**, S. G. Loizou, G. J. Pappas, Translating temporal logic to controller specifications. In the *Proceedings of the 45th IEEE Conference on Decision and Control*, pp. 899-904, San Diego, California, December 2006
- (C37) **G. E. Fainekos**, H. Kress-Gazit and G. J. Pappas, Hybrid Controllers for Path Planning: A Temporal Logic Approach. In the *Proceedings of the 44th IEEE Conference on Decision and Control*, pp. 4885-4890, Seville, Spain, December 2005
- (C38) **G. E. Fainekos**, H. Kress-Gazit and G. J. Pappas, Temporal Logic Motion Planning for Mobile Robots. In the *Proceedings of the IEEE International Conference on Robotics and Automation*, pp. 2020-2025, Barcelona, Spain, April 2005
- (C39) S. Bayraktar, **G. E. Fainekos** and G. J. Pappas, Experimental Cooperative Control of Fixed-Wing Unmanned Aerial Vehicles. In the *Proceedings of the 43rd IEEE Conference on Decision and Control*, pp. 4292-4298, The Bahamas, December 2004, (**Invited paper**)

Other Peer-Reviewed Publications

This category includes: (1) indexed conference and workshop tutorials and abstracts and (2) workshop papers with on-line only proceedings.

- (W1) **B. Hoxha**, **H. Bach**, **H. Abbas**, **A. Dokhanchi**, Y. Kobayashi and **G. Fainekos**, Towards Formal Specification Visualization for Testing and Monitoring of Cyber-Physical Systems, *International Workshop on Design and Implementation of Formal Tools and Systems*, Lausanne, Switzerland, October 2014
- (W2) **H. Abbas**, **B. Hoxha**, **G. Fainekos**, J. V. Deshmukh[†], J. Kapinski[†], and K. Ueda[†], WiP Abstract: Conformance Testing as Falsification for Cyber-Physical Systems. In the *Proceedings of the ACM/IEEE 5th International Conference on Cyber-Physical Systems*, Berlin, Germany, April 2014
- (W3) **Bardh Hoxha**, **Houssam Abbas** and **Georgios Fainekos**, Benchmarks for Temporal Logic Requirements for Automotive Systems, In the *Proceedings of the Applied Verification for Continuous and Hybrid Systems*, Berlin, Germany, April 2014
- (W4) **Bardh Hoxha**, **Houssam Abbas** and **Georgios Fainekos**, Using S-TaLiRo on Industrial Size Automotive Models, In the *Proceedings of the Applied Verification for Continuous and Hybrid Systems*, Berlin, Germany, April 2014
- (W5) D. Del Vecchio, E. A. Lee, J.-F. Raskin, G. J. Pappas, **G. E. Fainekos**, D. Caveney and L. Caminiti, Partial Order Techniques for the Analysis and Synthesis of Hybrid and Embedded Systems. In the *Proceedings of the 46th IEEE Conference on Decision and Control*, pp. 156-170, New Orleans, Louisiana, December 2007

Invited Conference Papers (No peer review)

- (I1) Fraser Cameron, **Georgios Fainekos**, David M. Maahs and Sriram Sankaranarayanan, Towards a Verified Artificial Pancreas: Challenges and Solutions for Runtime Verification, Proceedings of *Runtime Verification*, LNCS 9333, pp 1-15, Springer, 2015
- (I2) **A. Dokhanchi**, A. Zutshi[‡], **R. T. Sriniva**, S. Sankaranarayanan, and G. Fainekos, Requirements driven falsification with coverage metrics. In the Proceedings of *Embedded Software*, Amsterdam, The Netherlands, Oct. 2015
- (I3) Sriram Sankaranarayanan, C. Miller[‡], R. Raghunathan[‡], H. Ravanbakhsh[‡] and **Georgios Fainekos**, Analyzing Insulin Infusion Pump Usage Strategies in Diabetic Patients, In the Proc. of *50th Annual Allerton Conference on Communication, Control, and Computing*, Monticello, IL, Oct. 2012
- (I4) **H. Abbas** and **G. Fainekos**, Convergence Proofs for Simulated Annealing Falsification of Safety Properties, In the Proc. of *50th Annual Allerton Conference on Communication, Control, and Computing*, Monticello, IL, Oct. 2012
- (I5) A. Banerjee[‡], S. K. S. Gupta, **G. Fainekos**, and G. Varsamopoulos, Towards Modeling and Analysis of Cyber-Physical Medical Systems. In Proc. of the *4th International Symposium on Applied Sciences in Biomedical and Communication Technologies*, Barcelona, Spain, 2011

Technical Reports

- (TR1) **Kangjin Kim**, **Georgios Fainekos** and Sriram Sankaranarayanan, On the Minimal Revision Problem of Specification Automata. *Technical Report arXiv:1404.2289*, 2014
- (TR2) **Kangjin Kim** and **Georgios Fainekos**, Revision of Specification Automata under Quantitative Preferences, *Technical Report arXiv:1402.3611*, 2014
- (TR3) **H. Abbas**, **B. Hoxha**, **G. Fainekos**, J. V. Deshmukh[†], J. Kapinski[†], and K. Ueda[†], Conformance Testing as Falsification for Cyber-Physical Systems. *Technical Report arXiv:1401.5200*, 2014
- (TR4) **Houssam Abbas** and **Georgios Fainekos**, Linear Hybrid System Falsification With Descent. *Technical Report arXiv:1105.1733*, 2011
- (TR5) **G. E. Fainekos**, A. Girard, H. Kress-Gazit and G. J. Pappas, Temporal Logic Motion Planning for Dynamic Mobile Robots, *Technical Report MS-CIS-07-02*, Department of CIS, University of Pennsylvania, January 2007
- (TR6) **G. E. Fainekos**, A. Girard and G. J. Pappas, Hierarchical synthesis of hybrid controllers from temporal logic specifications, *Technical Report MS-CIS-07-01*, Department of CIS, University of Pennsylvania, January 2007
- (TR7) **G. E. Fainekos** and G. J. Pappas, Robustness of Temporal Logic Specifications for Finite State Sequences in Metric Spaces. *Technical Report MS-CIS-06-05*, Department of CIS, University of Pennsylvania, May 2006
- (TR8) **Georgios E. Fainekos**, An Introduction to Multi-Valued Model Checking. *Technical Report MS-CIS-05-16*, Department of CIS, University of Pennsylvania, September 2005
- (TR9) S. Bayraktar, **G. E. Fainekos** and G. J. Pappas, Hybrid Modeling and Experimental Cooperative Control of Multiple Unmanned Aerial Vehicles. *Technical Report MS-CIS-04-32*, Department of CIS, University of Pennsylvania, December 2004

Patent Applications

- (P1) U.S. Patent Application, Submitted on Nov. 2014. (**Fainekos** et al., “Guided Temporal Logic Testing of Cyber-Physical Systems”)
- (P2) U.S. Patent Application No 12/708,651, Publication No US 2010/0299651 A1 (published Nov. 25, 2010) (**Fainekos** et al., “Robust Testing for Discrete-Time and Continuous-Time System Models”)

Provisional Patent Applications

- (PP1) U.S. Patent Application No 61/900,866, Submitted on 11/6/2013. (**Fainekos** et al., “Guided Temporal Logic Testing of Cyber-Physical Systems”)
- (PP2) U.S. Patent Application No 61/890,368, Submitted on 10/14/2013. (**Srinivas** et al., “A graphical language and graphical user interface for linear temporal logic (LTL) motion and mission planning”)
- (PP3) U.S. Patent Application No 61/835,352, Submitted on 6/14/2013. (**Yang** et al., “Querying Parametric Temporal Logic Properties on Embedded Systems”)

RELEASED SOFTWARE

More information and links to software repositories can be found on my web-site:

<http://www.public.asu.edu/~gfaineko/research.html#software>

Software Toolboxes

1. TALIRO (TemporAl LogIc RObustness) tools is a suit of software tools for the analysis of continuous and hybrid dynamical systems using linear time temporal logics and their time constraint variants. Currently, the following tools are distributed on the web-page:

<https://sites.google.com/a/asu.edu/s-taliro/>

- S-TALIRO (Systems TALIRO) is a software toolbox for Matlab for the temporal logic falsification of Hybrid Automata and Matlab/Simulink models.
- TALIRO is a software toolbox for Matlab for the temporal logic robustness analysis of discrete time signals that take values in metric spaces. It is distributed with S-TALIRO and it can be utilized for both off-line and on-line testing and monitoring.

In 2012 and 2014, S-TaLiRo was nominated by the industry as a technological breakthrough for research contacted within the ASU National Science Foundation Industry-University Collaborative Research Center (NSF/IUCRC) Center for Embedded Systems (CES). The nominations can be found in the reports:

http://faculty.washington.edu/scottcs/NSF/2012/NSF_Compndium_2012-WEB.pdf

http://faculty.washington.edu/scottcs/NSF/2014/NSF_Compndium_2014.pdf

2. Temporal Logic Minimal Specification Revision and Planning (MSRP) Toolbox is a toolbox that provides tools for Linear Temporal Logic (LTL) planning and specification revision. It supports weighted finite system abstractions and specifications with user preferences.

<https://www.assembla.com/spaces/temporal-logic-specification-revision-and-planning-toolbox/wiki>

3. DisCoF is a software toolbox for cooperative pathfinding in distributed robotic systems with limited sensing and communication range. DisCof targets applications where large numbers of robots must navigate in cluttered environments. In such scenarios, the robots will need to locally coordinate and collaborate in order to be able to resolve deadlocks.

<https://www.assembla.com/spaces/discof/wiki>

4. Model-Based Development for Multi-iRobot Toolbox (MBDMIRT) is a Matlab/Simulink toolbox for the simulation of multiple iRobots where the control logic is defined using Stateflow charts. MBDMIRT can also be used for controlling groups of physical iRobots.

<https://www.assembla.com/code/mbdmirt/subversion/nodes>

5. LTLvis is a graphical specification environment to create high level motion plans to control robots by converting a visual representation of the motion/task plan into an Linear Temporal Logic specification. The visual interface is built on the Android tablet platform and provides functionality to create task plans through a set of well defined gestures and on screen controls.

<https://www.assembla.com/spaces/ltlvis/wiki>

6. Simulink/Stateflow Interface for NAO allows the Model Based Development of NAO software. The main benefit of the interface is that it allows both remote control of the NAO robot as well as code generation in a transparent way.

<https://www.assembla.com/spaces/simulink-stateflow-interface-for-nao/wiki>

7. LTL2BA_CPSLab is a modification of the popular LTL2BA tool so it can be used in specification revision applications. LTL2BA translates Linear Temporal Logic (LTL) specifications into finite automata with Buchi acceptance conditions. LTL2BA_CPSLab also provides information regarding which atomic propositions map to which symbols on the finite automaton.

https://www.assembla.com/code/ltl2ba_cpslab/git/nodes

PRESENTATIONS

Note: Presentations given at the conferences listed above are not mentioned in this section. Some selected presentations can be found at: <http://www.public.asu.edu/~gfaineko/papers.html>.

Invited Colloquia and Talks

1. *Specification guided testing and verification for Cyber-Physical Systems*, University of California, San Diego, San Diego, Mar. 2015
2. *Specification guided testing and verification for Cyber-Physical Systems*, University of New Mexico, Albuquerque, Mar. 2015
3. *Formal property verification in a conformance testing framework*, TU Delft, Delft, Netherlands, November 2014
4. *Temporal Logic Testing and Verification for Cyber-Physical Systems*, University of Tokyo, Tokyo, Japan, June 2014
5. *Temporal Logic Testing and Verification for Cyber-Physical Systems*, University of Arizona, Tuscon, AZ, April 2014
6. *Graphical Interfaces and Automated User Feedback for Temporal Logic Motion Planning*, KTH Stockholm, Sweden, Mar. 2014
7. *Temporal Logic Testing and Verification for Cyber-Physical Systems*, Boston University, Boston, MA, June 2013
8. *Temporal Logic Testing and Verification for Cyber-Physical Systems*, KTH, Stockholm, Sweden, Nov. 2012
9. *Temporal Logic Testing and Verification for Cyber-Physical Systems*, Lund University, Lund, Sweden, Nov. 2012
10. *Robust testing and Testing robustness for Cyber-Physical Systems*, Department of Electrical Engineering, University of California at Los Angeles, CA, June 2011
11. *Robust testing and Testing robustness for Cyber-Physical Systems*, Department of Computer Science and Engineering, Washington University, USA, September 2010
12. *Testing and Verification of Cyber-Physical Systems*, Department of Computer Science, Columbia University, USA, May 2008
13. *Testing and Verification of Cyber-Physical Systems*, Department of Computer Science and Engineering, Arizona State University, USA, April 2008
14. *Testing and Verification of Cyber-Physical Systems*, NEC Laboratories, USA, March 2008

15. *Cyber-Physical Systems: Theory and Applications*, Department of Electrical and Computer Engineering, Stevens Institute of Technology, USA, February 2008
16. *Robustness of Temporal Logic Specifications (and an application to verification using simulation)*, Verimag, Grenoble, France, September 2006

Invitation only Workshops and Panels

1. Presentation on *Collaborative Vehicular Systems* and panel discussion on *Transportation CPS*, NSF CPS PI Meeting, Arlington, Virginia, Nov. 2015
2. Talk on *On-Line Monitoring for Temporal Logic Robustness*, Safe and Secure Systems and Software Symposium (S5), Dayton, Ohio, June 2015
3. Directors' roundtable on *Medical Devices*, Mathworks Research Faculty Summit, Boston, USA, June 2015
4. Chair of Working Group on *Discrete Event Systems*, Mathworks Research Faculty Summit, Boston, USA, June 2015
5. Talk on "*Formal property verification in a conformance testing framework*" at Toyota CPS Summit 2014, Los Angeles, CA, Dec. 2014
6. Talk on "*Specification guided testing and verification for Cyber-Physical Systems*" at Dagstuhl Seminar on "Automated Planning and Model Checking", Dagstuhl, Germany, Nov. 2014
7. Talk on "*Descending MTL Robustness*" at Dagstuhl Seminar on "Verification of Cyber-Physical Systems", Dagstuhl, Germany, Mar. 2014
8. Working Group on Test, Evaluation, Verification and Validation for Autonomy, Wright Brothers Institute, Dayton, OH, Jan. 2014
9. Working Group on "Testing and Verification", NSF Workshop on Transportation Cyber-Physical Systems, D.C., USA, Jan. 2014
10. Temporal Logic Testing, Verification and Motion Planning for Robotic Systems (talk, software tool demonstration and poster), 4th Workshop on Formal Methods for Robotics and Automation, Berlin, Germany, June 2013
11. Chair of Working Group on *The importance of tools in establishing an engineered systems science*, Mathworks Research Faculty Summit, Boston, USA, June 2013
12. Presentation and panelist in session *Potential Roles of Computing Clouds in CPS Control*, NSF Workshop on Cloud Computing for Cyber-Physical Systems, Washington DC, USA, Mar. 2013
13. Talk on *Automatically Modifying Conflicting Specifications*, Safe and Secure Systems and Software Symposium (S5), Fairborn, Ohio, June 2012
14. Talk on *Temporal Logic Testing for Hybrid Systems*, Japanese National Institute for Informatics (NII) Shonan Meeting: Hybrid Systems - Theory and Practice, Seriously, Shonan, Japan, April 2012
15. Talk on *Verification of Control Applications using S-TaLiRo*, Workshop on Control systems and software Verification and Validation, Georgia Tech, Oct. 2011

Invited Talks and Outreach to Industry

1. *Requirements driven falsification with coverage metrics*, Toyota Motor Corporation, Shizuoka, Japan, July 2015

2. *Specification guided testing and verification for Cyber-Physical Systems*, Honeywell, Dec. 2014
3. *Specification guided testing and verification for Cyber-Physical Systems*, Bosch, Germany, Nov. 2014
4. *Specification guided testing and verification for Cyber-Physical Systems*, United Technologies Research Center, Hartford, CT, Nov. 2014
5. *Specification guided testing and verification for Cyber-Physical Systems*, Model Based Systems Engineering and Model Based safety Analysis Seminar, Boeing, Nov. 2014
6. *Temporal Logic Testing and Verification for Cyber-Physical Systems*, Toyota Motor Corporation, Shizuoka, Japan, June 2014

Invited Sessions at Conferences (Presentation only)

1. *Temporal Logic Testing for Cyber-Physical Systems*, INFORMS Annual Meeting, Phoenix, AZ, Oct. 2012

Tutorials

1. *Automotive control design bug-finding with the S-TaLiRo tool*, Tutorial session on “Simulation-Guided Approaches for Verification of Automotive Powertrain Control Systems”, American Control Conference, Chicago, USA, July 2015
2. *Temporal Logics over Lattices and Applications*, Tutorial session on “Partial order techniques for the analysis and synthesis of hybrid and embedded systems”, IEEE Conference on Decision and Control, New Orleans, USA, December 2007

ASU Seminars, Workshops and Guest Lectures

1. *Your car run's on code: Can you trust it?* ASU Math Club, ASU, Sept. 2014
2. *Your car run's on code: Can you trust it?* SCIDSE Faculty Seminar Series, ASU, Sept. 2014
3. *Model based design for Cyber-Physical Systems*, Professional Seminar, Nanoscience and Nanotechnology, ASU, Apr. 2013
4. *Ensuring that agile robots can maximally accomplish their tasks*, Piper Health Solutions Workshop on Rehabilitation Robotics, ASU, Feb. 2013
5. *Ensuring that agile robots can maximally accomplish their tasks*, Computational Mathematics Seminar, ASU, Jan. 2013
6. *Temporal Logic Testing and Verification for Cyber-Physical Systems*, Dynamics and Controls Colloquium, ASU, Dec. 2012

Defenses

1. *Robustness of Temporal Logic Specifications*, PhD thesis defense, University of Pennsylvania, USA, June 2008. Committee: Rajeev Alur (Chair), Edmund M. Clarke (External), Insup Lee, and Oleg Sokolsky
2. *Multi-Valued Model Checking*, In-depth examination, University of Pennsylvania, USA, June 2005. Committee: Rajeev Alur (Chair), Insup Lee and George J. Pappas
3. *Ant Colony Optimization: Applications to discrete and continuous problems*, Thesis defense, National Technical University of Athens, Greece, September 2001. Committee: K. Giannakoglou (Chair), K. Mathioudakis, K. Papailiou

Posters

1. **Houssam Abbas, Bardh Hoxha, Georgios Fainekos** and Koichi Ueda[†], Robustness-Guided Temporal Logic Testing for Stochastic Hybrid Systems, *International Conference on Hybrid Systems: Computation and Control*, Berlin, Germany, April 2014
2. **H. Abbas, B. Hoxha, G. Fainekos**, J. V. Deshmukh[†], J. Kapinski[†], and K. Ueda[†], Conformance Testing as Falsification for Cyber-Physical Systems. *ACM/IEEE 5th International Conference on Cyber-Physical Systems*, Berlin, Germany, April 2014
3. H. Kress-Gazit, **G. E. Fainekos** and G. J. Pappas, Temporal Logic Motion Planning for Mobile Robots, *Hybrid Systems : Computation and Control*, Santa Barbara, California, March 2006
4. **G. E. Fainekos**, H. Kress-Gazit and G. J. Pappas, Temporal Logic Motion Planning for Mobile Robots, *Graduate Research Symposium*, University of Pennsylvania, March 2005 (**Best poster award**)

TEACHING EXPERIENCE

- **Instructor**, Arizona State University, USA

- **CSE 494 (Fall 2015): Introduction to Mobile Robotics**
Number of students: 28
- **CSE 355 (Spring 2015): Introduction to Theoretical Computer Science**
Number of students: 125
- **CSE 574 (Fall 2014): Planning and Learning Methods in AI**
Number of students: 15; Some student comments: ** Fascinating topics, particularly the robotics stuff. * The content is interesting. Gives a good start to understand how to present and do projects. * Professor used very good ideas and material to develop student's interest in the course.*
- **CSE 355 (Spring 2014): Introduction to Theoretical Computer Science**
Number of students: 97; Some student comments: ** The course structure is gold standard. * [Dr. Fainekos] provides TONS of resources and uses technology effectively in lecture. He is enthusiastic and manages to interest students in what would otherwise be fairly boring content. * The effort the professor put into relating the theoretical material to application was very nice. This is especially important in a theory class because its easy to get lost in the level of abstractness. * Dr Fainekos is an excellent teacher, probably the most prepared instructor I have studied under at ASU. * Even though I'm not particularly interested in the topic it was one of the best run/organized classes I have taken at ASU. * Hard material. Great instructor.*
- **CSE 355 (Fall 2013): Introduction to Theoretical Computer Science**
Number of students: 78; Some student comments: ** Amazing enthusiasm and energy. Puts in more than 100% in preparing the students. * [I liked most] Instructor's passion for teaching and the quality of study material provided. * Prof Fainekos is a easy going professor and always engages in good interaction with students. He relates his teachings to any application possible and most importantly makes the classes enjoyable and easy to learn !*
- **CSE 591 (Spring 2013): Introduction to Algorithmic Robotics**
Number of students: 14; Some student comments: ** The content is very direct and easy to see applications * It was a fun subject * Great class!*
- **CSE 355 (Fall 2012): Introduction to Theoretical Computer Science**
Number of students: 63; Some student comments: ** [I liked most] The amount of group and individual Homeworks covering the exercises in the textbook were really good and would be highly recommended by me to all other professors to follow. * Although this was a prerequisite for me since I changed streams, this was one of the BEST class I've attended here at ASU. * Best teacher for 355 by far. Teaches everything well, prepares everything well, has students come prepared and makes sure they are prepared, he works with the students and all the material is posted before hand.*

- **CSE 522 (Spring 2012): Real-time Embedded Systems**
Number of students: 31; Some student comments: ** Overall the best course with the best professor. * The professor was able to help the students almost always which I liked the most. * Great course.*
- **CSE 522 (Spring 2012): Real-time Embedded Systems (On-line)**
Number of students: 10; Some student comments: ** I liked the fact that the class covered a variety of concepts concerning real-time embedded systems. From the basics to more advanced concepts. * Subject areas were interesting and having real life applications made the learning experience more concrete and interesting. * Professor Fainekos was very responsive and answered emails promptly. He was very helpful and gave good feedback and clarification on concepts when asked.*
- **CSE 355 (Fall 2011): Introduction to Theoretical Computer Science**
Number of students: 63; Some student comments: ** Most thought provoking class I have had yet. * He is very encouraging and very helpful. He is very clear about the exams and what is expected out of the students. Can't find a helpful professor like this !!! * Exams and quizzes are well designed. The instructor gave us sample each time before exam or quiz.*
- **CSE 522 (Spring 2011): Real-time Embedded Systems**
Number of students: 33; Some student comments: ** Very broad topic allowed grasping the big picture while still tying together several diverse fields of study. * Professor Fainekos is a very good professor. He always comes to class very prepared. * Good course in general. The instructor was helpful and showed a lot attention to students' progress.*
- **CSE 522 (Spring 2011): Real-time Embedded Systems (On-line)**
Number of students: 24; Some student comments: ** Dr Fainekos brought a lot of insight into the material from his experiences, and really did a good job putting together great slides to cover the material. * The instructor is very friendly, wants his students to learn, and communicates the material very well. The material is fun and challenging. * Instructor was fair and seemed to genuinely care whether his students learned the material.*
- **CSE 355 (Fall 2010): Introduction to Theoretical Computer Science**
Number of students: 49; Some student comments: ** Teacher was very enthusiastic about the course and us learning. * The professor was able to make difficult concepts easy to learn. The professor was great! * Not a huge fan of the material, the professor does a good job of making it interesting though.*
- **CSE 591 (Spring 2010): Theoretical aspects of Cyber-Physical Systems**
Number of students: 16; Some student comments: ** The course is a challenge. * This course attempts to build on necessary PhD skills.*
- **CSE 355 (Fall 2009): Introduction to Theoretical Computer Science**
Number of students: 26; Some student comments: ** This class made me enjoy and appreciate theory. * Overall a great class and instructor. * Keep it up man!*
- **Teaching Assistant**, University of Pennsylvania, USA
 - CSE 110 (Spring 2004): *Introduction to Programming (in C)*, Responsibilities: recitations (3 hour lectures per week), office hours, homeworks and exams, grading
 - CSE 390/MEAM 420 (Fall 2003): *Robotics*, Responsibilities: office hours, homeworks and exams, preparation of labs (using Lego Mindstorms), grading

STUDENTS

Current Students

- **PhD**

1. Adel Dokhanchi (Full time student since Fall 2012, CIDSE, ASU)
Research: *Temporal Logic Monitoring and Verification for Embedded Control Software.*

2. Bardh Hoxha (Full time student since Fall 2011, CIDSE, ASU)
Research: *Statistical methods for property exploration of Cyber-Physical Systems.*
3. Kangjin Kim (Full time student since Fall 2010, CIDSE, ASU)
Research: *Human-Robot Interaction through Temporal Logics: planing, specification revision and graphical interfaces.*
4. Ganesh P Kumar (Full time student since Fall 2012, CIDSE, ASU, Co-advised with Spring M. Berman)
Research: *Robot swarming.*
5. Dylan Lusi (Full time student since Fall 2015, CIDSE, ASU)
Research: *Process algebras for Cyber-Physical Systems.*
6. C. Erkan Tuncali (Full time student since Spring 2014, CIDSE, ASU)
Research: *Parallelization of embedded control software through Model-Based Design.*
7. Shakiba Yaghoubi (Full time student since Spring 2016, CIDSE, ASU)
Research: *Optimal control techniques for verification of Cyber-Physical Systems.*

- **MS**

1. Joseph Campbell (Part-time student since Spring 2014, CIDSE, ASU)
Research: *Multi-robot planning and control for ground robots.*
2. Rahul Thekkalore Sriniva (Full-time student since Fall 2014, CIDSE, ASU)
Research: *Test coverage for functional requirements for Cyber-Physical Systems.*
3. Xiaotong Zhang (Full-time student since Fall 2014, CIDSE, ASU)
Research: *Multi-robot planning and control for aerial robots.*
4. Wei Wei (Part-time student since Fall 2013, CIDSE, ASU)
Research: *Graphical user interfaces for multi-robot control.*

- **BS (REU & FURI supported)**

REU: Research Experience for Undergraduates (NSF supported)

FURI: Fulton Undergraduate Research Initiative (ASU supported)

1. Julian De Ocampo (NSF REU 2015-16, Junior, CIDSE, ASU).
Project: *Development of a multi-robot platform for automated warehouse applications.*

Alumni

Further information on the CPSLab's alumni can be found on my web site:

<http://www.public.asu.edu/~gfaineko/misc.html#alumni>

- **PhD**

1. Houssam Abbas (Spring 2015, ECEE, ASU, Co-advised with Tolga Duman).
Thesis title: *Test-Based Falsification and Conformance Testing for Cyber-Physical Systems.*
First placement: Postdoctoral researcher at University of Pennsylvania (mLab/PRECISE Center)

- **MS**

1. Shashank Srinivas (Spring 2013, CIDSE, ASU).
Thesis title: *A Graphical Language for LTL Motion and Mission Planning.*
First job placement: Intel, USA.
Public software release: LTLvis: A Graphical Language for LTL Motion and Mission Planning.
2. Hengyi Yang (Spring 2013, CIDSE, ASU).
Thesis title: *Dynamic Programming algorithm for Computing Temporal Logic Robustness.*
First job placement: Amazon, USA.
Public software release: Contributed to the S-TALIRO tools.

3. Ramtin Kermani (Fall 2013, CIDSE, ASU).
Thesis title: *Model-based Design, Simulation and Automatic Code Generation For Embedded Systems and Robotic Applications*.
First job placement: OSIsoft, USA.
Public software release: NAOMBD: NAO Model-Based Development for Simulink / Stateflow.
4. Anurag Kamasamudram (Fall 2013, CIDSE, ASU, Co-advised with Srikanth Saripalli)
Thesis title: *Smooth path planning using splines for unmanned planetary vehicles*.
First job placement: Co-Founder/Lead iOS Developer at Dezapp,LLC
5. Parth Pandya (Spring 2013, CIDSE, ASU).
Thesis title: *A Modular ROS package for Linear Temporal Logic based Motion Planning*.
First job placement: Qualcomm, USA
6. Shih-Kai Su (Fall 2012, CIDSE, ASU).
Thesis title: *Model-Based Development for Multi-iRobot Simulation and Control*.
First job placement: Cypress Technology Co., Taiwan
Public software release: MBDMIRT: Model-Based Development for Multi-iRobot Toolbox.
7. Yashwanth Singh Rahul Annapureddy (Fall 2011, CIDSE, ASU).
Thesis title: *S-TaLiRo: A tool for Temporal Logic Falsification for Hybrid Systems*.
First job placement: Mathworks, USA.
Public software release: Contributed to the S-TALIRO tools.

- **BS (REU & FURI supported)**

REU: Research Experience for Undergraduates (NSF supported)

FURI: Fulton Undergraduate Research Initiative (ASU supported)

1. William Duong (NSF REU 2014-15, Junior, CIDSE, ASU).
Project: *Development of a multi-robot platform for automated warehouse applications*.
Highlights:
(a) Co-author of CASE 2015 conference paper
2. Sami Mian (NSF REU 2013-14, Sophomore, CIDSE, ASU).
Projects: *Development of a multi-robot platform for automated warehouse applications; Development of a humanoid robot driving a small electric vehicle*.
Highlights:
(a) Honorable award in the Cornell Cup competition 2014
(b) Research internship for Fall 2014 in Singapore-MIT Alliance for Research and Technology in the Autonomous Vehicles Group
3. Rick Ahlf (NSF REU & FURI 2013-14, SEMTE, ASU).
Project: *Model Development and Testing for Automotive Systems*.
First job placement: Smartplane Inc.
4. Hoang Bach (NSF REU 2013-14, CIDSE, ASU).
Project: *Graphical User Interface for Cyber-Physical System Requirements*.
Highlights:
(a) Co-author of DIFTS 2014 workshop paper
(b) Internship after CPS Lab: Intel
5. Jorge Arce Mendoza (NSF REU 2012-13, Senior, ASU).
Project: *Parallelization of Embedded Control Applications on Multi-core Architectures*.
First job placement: General Dynamics C4 Systems.
6. Scott Jones (NSF REU 2012-13, ECEE, ASU)
Project: *Temporal Logic Testing for Stochastic Cyber-Physical Systems*.
Internship after CPS Lab: Raytheon.

Other student advising:

• Competition Teams

1. Cornell Cup USA 2014

Honorable mention award

Team: Mila Arezina, Joe Boeding, Bijan Fakhri, Sami Mian (Team lead), Ryan James Sterry, and Cameron Stewart (advisor)

Team name: NAO Navigators

Project entry: *Electric vehicle driven by an autonomous NAO humanoid robot.*

2. RoboCup Standard Platform League (SPL)

Setup the team, acquired the robots, recruited students, and mentoring the team to participate to its first competition in 2015.

• Capstone teams supported

1. Brandon Caffie, William Haselwood, Nicholas Keohane, Michael Storto, and Rodolfo Torres (Fall 2014 - Spring 2015, CIDSE, ASU).

Project title: *RoboCup Standard Platform League (SPL).*

2. Christopher Hamilton, Jason Ryan, James Bird, and Lauren Smith (Spring 2014, CIDSE, ASU).

Project title: *Touch screen interface for a game controlling a virtual UAV.*

3. Mila Arezina, Joe Boeding, Bijan Fakhri, Ryan James Sterry, and Cameron Stewart (Fall 2013 - Spring 2014, CIDSE, ASU).

Project title: *Electric Vehicle Driven by a Humanoid NAO Robot.*

4. Andrew Becker, Jesse Deng, Jaclyn Lim, Jorge Mendoza, Liangcheng Tang, and Wei Wei (Fall 2012 - Spring 2013, CIDSE, ASU).

Project title: *Touch screen interface for controlling a UAV.*

5. Nikunj Dixit, Anurag Kamasamudram, Wenyang Li, and Narendra Patel (Fall 2011 - Spring 2012, CIDSE, ASU).

Project title: *Touch screen interface for Human-Robot Interaction.*

6. Blake Birmingham, Nathan Kiel, Patrick Murphy, Mark Nixon, Michelle Paynes, and Marcus Yazzie (Fall 2010 - Spring 2011, CIDSE, ASU).

Project title: *Multi-Robot Experimental Platform.*

• Honors Contracts (Barrett Honors College)

1. Christopher Recher (Spring 2014, CIDSE, ASU), Topic: *Model Checking and Program Verification in Spin*

2. Ramin Tadayon (Fall 2009, CIDSE, ASU), Topic: *Probabilistic Automata*

• On-line MCS Applied Projects

1. Jonathan Duncan (Spring 2014, CIDSE, ASU), Applied project title: *Sound Tracker: Human in the Loop Sound Tracking Robot*

2. Steve Miskovetz (Fall 2013, CIDSE, ASU), Applied project title: *A home automation and monitoring system built on a framework for expandability*

3. Andrew Tomlinson (Spring 2013, CIDSE, ASU), Applied project title: *Autonomous Home Watering System*

4. Shawn Cook (Fall 2012, CIDSE, ASU), Applied project title: *Dreadful Bacon: Online drum machine application that employs intuitive user interface gestures and graphical tree structures to create musical drum beats*

5. Benjamin Jones (Fall 2011, CIDSE, ASU), Applied project title: *Automated Control of Multistage Evaporative Chilling for Residential Cooling Systems*

Thesis Committees

- **ASU PhD Thesis committee member:**

1. Sai Hemachandra Vemprala (in progress, SESE, ASU, Advisor: Srikanth Saripalli)
2. Aravindhan Krishnan (in progress, SESE, ASU, Advisor: Srikanth Saripalli)
3. Young Wn Song (in progress, CIDSE, ASU, Advisor: Yann-Hang Lee)
4. Randall Hellman (in progress, SEMTE, ASU, Advisor: Veronica Santos)
5. Michael Jonas (in progress, CIDSE, ASU, Advisor: Sandeep Gupta)
6. Michael Bartholomew (in progress, CIDSE, ASU, Advisor: Joohyung Lee)
7. Jun Shen (in progress, CIDSE, ASU, Advisor: Rida Bazzi)
8. Yucong Lin: Moving Obstacle Avoidance for Unmanned Aerial Vehicles (2015, SESE, ASU, Advisor: Srikanth Saripalli)
9. Mark Ison: Motor Learning and Flexible Muscle Synergies (2015, SEMTE, ASU, Advisor: Panagiotis Artemiadis)
10. Yunsong Meng: Answer Set Programming and Other Computing Paradigms (2013, CIDSE, ASU, Advisor: Joohyung Lee)
11. Ayan Banerjee: Model Based Safety Analysis and Verification of Cyber-Physical Systems (2012, CIDSE, ASU, Advisor: Sandeep Gupta)

- **External PhD Thesis reviewer / committee member:**

1. Meng Guo (in progress, Electrical Engineering, KTH, Sweden, Advisor: Dimos V. Dimarogonas)
2. Sadegh E.S. Soudjani : Formal Abstractions for Automated Verification and Synthesis of Stochastic Systems (2014, Delft Center for Systems & Control, TU Delft, Netherlands, Advisors: Alessandro Abate and Hans Hellendoorn)

- **ASU Master Thesis & Applied Project committee member:**

1. Savitha Sundaramoorthi : Eclipse BIRT Plug-ins for Dynamic Piecewise Constant and Event Time Series (2015, CIDSE, ASU, Advisor: Hessam Sarjoughian)
2. Tushar Rawat : Enabling Multithreaded Applications on Hybrid Shared Memory Many-core Architectures (2014, CIDSE, ASU, Advisor: Aviral Shrivastava)
3. Savitha Sundaramoorthi : Eclipse BIRT Plug-ins for Dynamic Piecewise Constant and Event Time Series (2014, CIDSE, ASU, Advisor: Hessam Sarjoughian)
4. Girish Rao Bulusu : Asymmetric Multiprocessing Real Time Operating System on Multicore Platforms (2014, CIDSE, ASU, Advisor: Yann-Hang Lee)
5. Xiaolan Wang : Leveraging Metadata for Extracting Robust Multi-Variate Temporal Features for Time Series (2013, CIDSE, ASU, Advisor: Kasim Candan)
6. Sai Hemachandra Vemprala : Leveraging Metadata for Extracting Robust Multi-Variate Temporal Features for Time Series (2013, SESE, ASU, Advisor: Srikanth Saripalli)
7. Nirav Jadav : Applied project (2013, Nanoscience, ASU, Advisors: Jingyue Liu and John Venables)
8. He Lu : Multiple Thread Replay Debugger with Human Behavior on Android Platform (2012, CIDSE, ASU, Advisor: Yann-Hang Lee)
9. James Melkon Smith : Scalable Knowledge Interchange Broker: Design and Implementation for Semiconductor Supply Chain Systems (2012, CIDSE, ASU, Advisor: Hessam Sarjoughian)
10. Barry Lumpkin : A High Level Language for Human Robot Interaction (2012, CIDSE, ASU, Advisor: Chitta Baral)
11. Fei Hong : Soft Error Resilient Redundant CMP Architecture (2011, CIDSE, ASU, Advisor: Aviral Shrivastava)
12. Sailesh Kandula : Model Based Safety Analysis of Cyber Physical Systems (2010, CIDSE, ASU, Advisor: Sandeep Gupta)

SERVICE AND PROFESSIONAL ACTIVITIES

- ASU Internal Service
 - 2015-16:
 - * Computer Systems Engineering (CSE) Undergraduate Program Committee (UPC)
 - * Chair of the robotics faculty search committee
 - * Computer Engineering program qualifying exams
 - 2014-15:
 - * Chair of the robotics faculty search committee
 - 2013-14:
 - * Robotics faculty search committee
 - * Computer Systems Engineering (CSE) Undergraduate Program Committee (UPC)
 - * Computer Engineering (CEN) Admissions Committee
 - 2012-13:
 - * Computer Systems Engineering (CSE) Undergraduate Program Committee (UPC)
 - 2011-12:
 - * Computer Science (CS) Admissions Committee
 - 2010-11:
 - * Computer Systems Engineering (CSE) Undergraduate Program Committee (UPC)
- Local Community Outreach
 - Co-organization of the first “La vida robotics” (<http://www.lavidarobots.org/>) meeting at “Local Motors”, June 2015: Presentations on robotics and hands-on robotics labs
 - Robotics exhibit at ASU Engineering Open House Family Day 2012
- Professional Society Service
 - 2015 to date: IEEE Control Systems Society (CSS) representative in the IEEE Systems Council
- Associate Editor
 - 2012 to date: ACM SIGBED Review (ISSN: 1551-3688) (<http://sigbed.seas.upenn.edu/>)
- Conference Organizer:
 - Program Chair (with Alessandro Abate): Hybrid Systems: Computation & Control (HSCC); Part of CPSWeek 2016.
- Workshop Organizer:
 - Program Chair (with E. Goubault and S. Putot): Numerical Software Verification (NSV) III; Affiliated with the Federated Logic Conference (FLOC) 2010.
 - Program Chair (with E. Goubault and S. Sankaranarayanan): Numerical Software Verification (NSV) II: Verification of Cyber-Physical Software Systems; Affiliated with the Cyber-Physical Systems (CPS) Week 2009.
- Conference Organizing Committee member:
 - Publication chair: International Conference on Cyber-Physical Systems (ICCPS) 2015
 - Registration chair: Cyber-Physical Systems (CPS) Week 2013
 - Exhibit chair: Annual Conference of IEEE Industrial Electronics (IECON) 2010

- Program Committee member:
 - 2016:
 - * International Conference on Cyber-Physical Systems (ICCPS)
 - * International Workshop on Formal Engineering approaches to Software Components and Architectures (FESCA)
 - * AAAI-16 Workshop on Planning for Hybrid Systems (PlanHS-16)
 - * Workshop on Monitoring and Testing Cyber-Physical Systems (MT-CPS)
 - * Numerical Software Verification (NSV)
 - 2015:
 - * IFAC Conference on Analysis and Design of Hybrid Systems (ADHS)
 - * International Workshop on Design, Modeling and Evaluation of Cyber Physical Systems (CyPhy)
 - * ACM Hybrid Systems: Computation and Control (HSCC)
 - * International Conference on Cyber-Physical Systems (ICCPS)
 - * IEEE International Conference on Robotics and Automation (ICRA)
 - * ACM SIGPLAN/SIGBED Conference on Languages, Compilers, Tools and Theory for Embedded Systems (LCTES)
 - * International Workshop on Model Checking and Automated Planning (MOCHAP)
 - * International Conference on Runtime Verification (RV)
 - 2014:
 - * Workshop on Applied Verification for Continuous and Hybrid Systems (ARCH)
 - * Formal Modeling and Analysis of Timed Systems (FORMATS)
 - * Workshop on Hybrid Autonomous Systems (HAS)
 - * ACM Hybrid Systems: Computation and Control (HSCC)
 - * IEEE International Conference on Robotics and Automation (ICRA)
 - * IEEE Real-Time Systems Symposium (RTSS) CPS
 - * Workshop on Synthesis of Continuous Parameters (SynCoP)
 - * Summer Computer Simulation Conference (SCSC)
 - 2013:
 - * Workshop on Cyber-Physical Systems Education (CPS-Ed)
 - * Formal Modeling and Analysis of Timed Systems (FORMATS)
 - * ACM Hybrid Systems: Computation and Control (HSCC)
 - * Temporal Representation and Reasoning (TIME)
 - 2012:
 - * Formal Methods for Industrial Critical Systems (FMICS)
 - * Formal Modeling and Analysis of Timed Systems (FORMATS)
 - * Numerical Software Verification (NSV)
 - 2011:
 - * IEEE Real-Time Systems Symposium (RTSS) WiP
 - 2010:
 - * Logics for System Analysis (LfSA)
 - * IEEE Real-Time Systems Symposium (RTSS)
 - 2009:
 - * Interaction and Concurrency Experiences (ICE)
 - * Quantitative Formal Methods (QFM)

- NSF Panels
 - Cyber-Physical Systems (CPS)
 - National Robotics Initiative (NRI)

- Reviewer for Journals (alphabetical order):
 - ACM Transactions on Embedded Computing Systems
 - Automatica
 - Discrete Event Dynamic Systems
 - Formal Methods in System Design
 - IEEE Control Systems Magazine
 - IEEE Robotics and Automation Magazine
 - IEEE Systems, Man and Cybernetics
 - IEEE Transactions on Automatic Control
 - IEEE Transactions on Automation Science and Engineering
 - IEEE Transactions on Control Systems Technology
 - IEEE Transactions on Control of Network Systems
 - IEEE Transactions on Cybernetics
 - IEEE Transactions on Robotics
 - IEEE Transactions on Software Engineering
 - International Journal of Robotics Research
 - Robotics and Autonomous Systems
 - Theoretical Computer Science

- (Regular) Reviewer for Major Conferences and Workshops (alphabetical order):
 - American Control Conference (ACC)
 - Analysis and Design of Hybrid Systems (IFAC ADHS)
 - Conference on Automation Science and Engineering (IEEE CASE)
 - Computer Aided Verification (CAV)
 - Conference on Decision and Control (IEEE CDC)
 - Embedded Systems Software (ACM EMSOFT)
 - European Control Conference (ECC)
 - Formal Modeling and Analysis of Timed Systems (FORMATS)
 - Hybrid Systems: Computation and Control (ACM HSCC)
 - Industrial Electronics (IEEE IECON)
 - International Conference on Intelligent Robots and Systems (IEEE/RSJ IROS)
 - International Conference on Robotics and Automation (IEEE ICRA)
 - Logic in Computer Science (LICS)
 - Mediterranean Control Conference (MED)
 - Multi-conference on Systems and Control (MSC)
 - Real-Time Systems Symposium (IEEE RTSS)

- Graduate Student Representative for the academic year 2007-08 at the University of Pennsylvania (Attending faculty meetings; Member of the committee for the appointment of the new department Chairperson)
- Professional Memberships:
 - Association for Computing Machinery (ACM): Member 2008-, Student Member 2006-08.
Special Interest Group: Embedded Systems (SIGBED).
 - Institute of Electrical and Electronics Engineers (IEEE): Member 2008 -, Student Member 2004-08.
Societies: Robotics and Automation Society (RAS); Control Systems Society (CSS).
 - American Society Of Mechanical Engineers (ASME) Member 2008-10, Student Member 2006-08.
 - Member of the Technical Chamber of Greece 2002-2012 (as Mechanical Engineer)

OTHER EDUCATIONAL BACKGROUND

- **Launching of Ariane V.** Ecole Centrale Paris, Board of European Students of Technology (BEST) Spring course 2002, Paris, France.
Classes on the propulsion system of Ariane V and visits to ESA, Snecma
- **Usability Engineering.** Chalmers University of Technology, Board of European Students of Technology (BEST) Summer course 2001, Gothenburg, Sweden.
Classes on usability engineering and visits to SKF

COMPUTER KNOWLEDGE & FOREIGN LANGUAGES

- **Programming:** C, C++, Fortran, Visual Basic, OCaml
- **CAD/FEA:** SolidWorks, CosmosWorks, AutoCAD, Mechanical Desktop, Pro Engineer
- **Model Checking Tools:** SPIN, SMV
- **Modelling and Prototyping:** MATLAB, Simulink, LabView, Mathematica
- **Greek:** Native, **English:** Fluent (Cambridge Certificate of Proficiency in English), **German, Spanish, Japanese:** beginner

EXTRA-CURRICULAR ACTIVITIES

- Open Sea Sailing (former member of the Hellenic Offshore Racing Club, Greece)
- Horse Riding (former member of the Horse Riding Club of Mesogeion, Greece)
- Basketball (During High-School: member of Kronos Basketball Team, Greece)
- Playing the saxophone (former member of Agios Dimitrios Municipality Band, Greece)