VISHNU TEJA KILARI

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EDUCATION

- Master of Science, Computer Science   Graduate Cumulative GPA:3.75/4
  Ira A. Fulton Schools of Engineering, Arizona State University   (August'11 - Present)
- B. Tech, Computer Science and Engineering   Cumulative GPA: 7.94/10
  Pondicherry University, India   (August'07 - April'11)

HONOURS

- Certified Professional hacker from Tech-Defense

COMPUTER AND PROGRAMMING SKILLS

- Operating Systems   : Windows 7, Unix, Ubuntu and Macintosh OS X
- Databases   : MS Access, Oracle, 4D Server, SQL Server and MYSQL
- Application package : MS-Office, Open-Office, Adobe Suite (Flash, Dreamweaver, Photoshop)
- Languages   : C, Java, C#, ASP.Net, HTML, JSP, PHP, Java Script and XML

WORK EXPERIENCE

- **Graduate Research Assistant,** Arizona State University.   **(Aug'11- Present)**
  - Working on development (Impact Analysis, Coding, Unit Testing) of the web sites and databases based on Java and 4D database which involves development and deployment of databases
  - Used 4D queries, views and stored procedures for analysis and validation of student data

PROJECTS

- **Developed a search engine for Semi- structured data** – This project involves extracting semi structured data such as tables from the web and building a search engine for such data. This search engine is built using Java, Python and Apache Lucene
- **Developed a SaaS application using Multi-tenant architecture and web services -** This **SaaS application** provides the functionality of database and workflow customization for different clients by using **multi-tenant architecture** at database level (Sales Force implementation). Developed the system using ASP.Net, C#, PHP, MYSQL and third party web services and implemented the multi-tenancy and deployed it onto ASU server
- Implementation of **multithreading in C** using non preemptive scheduling and user level threads, **implementation of semaphores** and building a **client server application** encompassing message passing
- **Matrix multiplication on an IBM Cell processor –** This project involves implementation of distributed matrix multiplication on Cell processor of 1 PPU and 8 SPU s. Each matrix is of the size 1000 x 1000
- **RRIP block replacement in Associative Caches –** The goal of the project is to improvise Static and Dynamic RRIP block replacement schemes in associative caches to increase the hit rate and implement the improvised versions and run them on a series of benchmarks. This project is implemented in C
- **Web based document management system**– Goal of the project is to facilitate management and access of all documents within an organization through internet. Implemented **access control**, **single sign on** (SSO) and functionalities such as read, update, delete, sharing, archiving, version control, sharing, encryption, version control, indexing of documents. Developed the system using Java, JSP, Struts and MYSQL
- **Design and implementation of networked smart energy meters for facilitating Energy Audit** – Design and implementation of a network of economical **prototype** (**self-designed**) of smart energy meters encompassing a current measuring IC driven by a micro controller which saves the energy log in its on-board EEPROM and uses the log for energy audit. Nominated as **"Best Project of the Year"**

COURSE WORK

- Design and Analysis of Algorithms, Computer Systems Security, Software security, Multiprocessor and Distributed Operating Systems, Software Design, Semi-Structured Data Management, Introduction to Artificial Intelligence and Computer Architecture

TECHNICAL ACTIVITIES

- Participated in Ethical Hacking and Information Security Workshop, Networking Workshop and Linux kernel workshop at **Indian Institute of Technology, Madras**
- Member of Association for Computer Systems Security- ACSS, ASU