Ziming Zhao is an assistant professor at the Golisano College of Computing and Information Sciences at the Rochester Institute of Technology. He directs the CyberspACe securiTy and forensic lab (CactiLab, http://CactiLab.info) at RIT, before which he was an assistant research professor at the Arizona State University and a co-director of the SEFCOM lab (http://sefcom.asu.edu). His research interests lie primarily in hardware-assisted security, system security and software security. He has published extensively in the aforementioned areas as well as in network security, usable security and threat intelligence analytics in top-tier security conferences and journals, including IEEE S&P, USENIX Security, NDSS, ACSAC, ESORICS, TISSEC, etc. He received the PhD degree in computer science from the Arizona State University in 2014. He was a recipient of best paper awards in ACM CODASPY and IEEE ITU Kaleidoscope. He was a general co-chair of ACM CODASPY 2018.

Ziming Zhao is looking for PhD students who are interested in hardware-assisted security, system security and software security to join the CactiLab. Students interested in security from Computer Science, Computer Engineering, Electrical Engineering, Software Engineering, or other related disciplines are encouraged to apply. Self-funded visiting students/scholars are also welcome to apply. Rochester Institute of Technology is a private institution that was founded in 1829. Golisano College of Computing and Information Sciences features one of the most comprehensive computing colleges in the nation with more than 130 faculty members. The PhD Program in Computing and Information Sciences was ranked #68 in the U.S. News and World Reports 2018 ranking of Best Doctoral Computer Science Programs.

Self-motivated students with good programming skills and solid background in any of the following areas are an excellent fit to the CactiLab:

- ARM TrustZone; Trusted Platform Module; Intel SGX (Assembly, C, etc.)
- Operating systems; Linux kernel development (Assembly, C, etc.)
- Hypervisor (Assembly, C, etc.)
- FPGA design and development (OpenCL, VHDL, Verilog, etc.)
- Android framework and kernel (Java, C, C++, etc.)
- Software-defined networking
- Program analysis and compiler
- Machine learning and data mining
- Automobile systems

The start dates of the positions are flexible, i.e., Spring or Fall semesters. If you are interested in the position, please feel free to contact Ziming at ziming.zhao@gmail.com.