

## Call for Papers

### International Workshop on Systems and Algorithms for Distributed Embedded Intelligence (SADEI 2015)

March 25-27, 2015, Taichung, Taiwan

This workshop will be held in conjunction with the 12th International Symposium on Autonomous Decentralized Systems (ISADS 2015, <http://isads2015.asia.edu.tw/>)

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Xia Zhu, Intel, USA

#### In ISADS submission site:

<https://www.easychair.org/conferences/?conf=isads2015>, choose SADEI track

#### Important Dates

Paper submission: 10/31/2014

Author notification: 11/30/2014

Final manuscript due: 12/31/2014

#### Contact information:

Please contact SADEI general chair or program chairs for paper submission issues.

Please contact ISADS local Arrangements team for issues related local arrangement issues.

#### Justification of the proposed topic:

SADEI 2015 is the premier interdisciplinary forum for the presentation of new advances and research results in the fields of systems and algorithm for distributed embedded intelligence. The workshop provides an opportunity for academic and industry professionals to discuss recent progress in the area of design, synthesis, implementation, test and analysis of embedded systems including digital, analog and mixed devices. The goals of this workshop are to provide a complete coverage of the areas outlined and to bring together the researchers from academic and industry as well as practitioners to share ideas, challenges, and solutions relating to the aspects of this field.

Papers describing systems and algorithms for distributed embedded Intelligence are solicited. SADEI 2015 focuses on distributed and embedded system, intelligence systems, and algorithms for distributed systems. Topics of interest include, but are not limited to:

- Memory Architectures for embedded multicore systems
- Compilers for heterogeneous embedded multicore systems.
- Programming models for embedded multicore systems
- Compiler for worst-case execution time analysis
- Distributed and Parallel Processing
- Embedded OS designs and performance tuning tools
- Embedded System Software & Optimization
- Intelligent human computer interfaces
- Intelligent devices and instruments
- Algorithm to analysis and simulation techniques and systems
- Application-Specific Processors and Devices
- Artificial Intelligence in distributed system
- Application-specific processors/devices
- Real-time system designs for embedded multicore environments
- Parallel and distributed computing
- Applications for Automobile electronics of multicore designs
- Internet of Intelligent Things and device as a service
- Emerging technologies/applications/principles/algorithms

#### Information for Authors:

Research papers should describe original work (not submitted or published elsewhere) and be up to 15 double-spaced pages (4,000 words) or 6 pages in IEEE double-column conference paper format. Papers should include: title, authors, affiliations, 150-word abstract and list of keywords. Please identify the contact author clearly, including name, position, mailing address, telephone number, and email address. At least one of the authors of each accepted paper must register and present the paper at ISADS 2015. Authors must submit their manuscripts electronically following the instructions at the ISADS 2015 web site at: <http://isads2015.asia.edu.tw> and choose the SADEI workshop when submitting.

